

Summary

Report Date	2025-06-18 16:51:52
Generated by	MemTest86 V11.3 Pro (64-bit)
Result	INCOMPLETE PASS

System Information

EFI Specifications	2.80
System	
Manufacturer	Applied Materials
Product Name	Compact Server 2.0
Version	0.9
Serial Number	Default string
BIOS	
Vendor	American Megatrends International, LLC.
Version	5.25
Release Date	11/15/2023
Baseboard	
Manufacturer	Applied Materials
Product Name	Compact Server 2.0
Version	1.0
Serial Number	Default string
CPU Type	12th Gen Intel Core i5-12500E
CPU Clock	2954 MHz [Turbo: 4098.4 MHz]
# Logical Processors	12 (6 enabled for testing)
L1 Cache	4 x 80K (136083 MB/s)
L2 Cache	4 x 1280K (28347 MB/s)
L3 Cache	18432K (12618 MB/s)
Memory	16170M (7467 MB/s)
RAM Configuration	DDR5 ECC 4788MT/s / x2 Channel / 40-39-39-76 / 1.100V
Number of RAM SPDs detected	1
SPD #0	16GB DDR5 1Rx8 ECC PC5-44800
Vendor Part Info	Advantech Co Ltd / SQR-SD5N16G5K6SEPB / 50630001
JEDEC Profile	5600MT/s 46-45-45-90 1.1V
Number of RAM slots	2
Number of RAM modules	1
DIMM A1	Empty slot
DIMM B1	16GB DDR5 1Rx8 ECC PC5-44800
Vendor Part Info	Advantech Co Ltd / SQR-SD5N16G5K6SEPB / 50630001
SMBIOS Profile	5600MT/s 1.1V

Result summary

Test Start Time	2025-06-18 16:50:20
Elapsed Time	0:01:06
Memory Range Tested	0x0 - 4AFC00000 (19196MB)
CPU Selection Mode	Parallel (All CPUs)
CPU Temperature Min/Max/Ave	56C/58C/57C
RAM Temperature Min/Max/Ave	
TSOD0	34C/40C/37C
Lowest memory speed	4786 MT/s (40-39-39-76)
Highest memory speed	4788 MT/s (40-39-39-76)
ECC Polling	Enabled
# Tests Completed	4/56 (7%)
# Tests Passed	4/4 (100%)

ECC Correctable Errors	0
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Test	# Tests Passed	Errors
Test 0 [Address test, walking ones, 1 CPU]	1/1 (100%)	0
Test 1 [Address test, own address, 1 CPU]	1/1 (100%)	0
Test 2 [Address test, own address]	1/1 (100%)	0
Test 3 [Moving inversions, ones & zeroes]	1/1 (100%)	0
Test 4 [Moving inversions, 8-bit pattern]	0/0 (0%)	0
Test 5 [Moving inversions, random pattern]	0/0 (0%)	0
Test 6 [Block move, 64-byte blocks]	0/0 (0%)	0
Test 7 [Moving inversions, 32-bit pattern]	0/0 (0%)	0
Test 8 [Random number sequence]	0/0 (0%)	0
Test 9 [Modulo 20, random pattern]	0/0 (0%)	0
Test 10 [Bit fade test, 2 patterns, 1 CPU]	0/0 (0%)	0
Test 11 [Random number sequence, 64-bit]	0/0 (0%)	0
Test 12 [Random number sequence, 128-bit]	0/0 (0%)	0
Test 13 [Hammer test]	0/0 (0%)	0

Last 10 Errors

2025-06-18 16:51:26 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:25 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:15 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:15 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:14 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:13 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:12 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:11 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:10 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

2025-06-18 16:51:10 - [ECC Errors] Test: 4, (Channel,Slot,Rank,Bank,Row,Col): (0,0,3,7,3FFFF,7FF), ECC Corrected: No, Syndrome: 0000, Channel-Slot: 0-0

Certification

This document certifies that the Tests described above have been carried out by a suitably qualified technician on the System described above.

Signed

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